2123

PATENT
(5181-98000/P6631)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/008,643
Filed: November 9, 2001
Inventor(s):
Steven A. Sivier
Carl B. Frankel
Carl Cavanagh
James P. Freyensee
Title: Synchronization of Distributed Simulation Nodes by Keeping Timestep Schedulers in Lockstep

§ Examiner: Unknown
§ Group/Art Unit: 2123
§ Atty. Dkt. No: 5181-98000

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Washington, DC 20231, on the date indicated below.

Lawrence J. Merkel
Name of Registered Representative

Signature 11/23/04 Date

STATUS INQUIRY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

On November 9, 2001, Applicants' undersigned agent filed the original application documents for the above-referenced application. Applicants have not yet received a first Office Action on the merits. Applicants hereby respectfully request the Patent Office to notify Applicant as to the status of this matter.

Should any fees be required, the Commissioner of hereby authorized to charge such fees to Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C. Deposit Account No. 501505/5181-98000/LJM.

Respectfully submitted,

Lawrence J. Merkel
Reg. No. 41,191

Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C.
P.O. Box 398
Austin, TX 78767-0398
(512) 853-8800
Date: 11/23/04